## Product Change Notice

## Introduction of F91C (0.8 Micron 68030) in Plastic Packages

Motorola Inc. will consider this change approved unless specific conditions of acceptance are provided in writing within 30 days of receipt of this notice.

| Issue Date | Effective Date | Notification \# |
| :--- | :---: | :---: |
| 23-Aug-95 | 24-May-95 | R00283 |


| Affected Change Categories |  | Affected Product Divisions |
| :---: | :---: | :---: |
| 68030 (All FE, RC suffixes) |  | Imaging and Storage |
| Additional Reliability Data |  | Samples |
| Included |  | Available |

For any questions concerning this notification:

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## Description and Purpose

Motorola is pleased to announce the extension of the F91C mask set ( 0.8 micron 68030) into the 124ld plastic pin grid array (RP) and 144ld thin quad flat pack (PV) packages. These packages are currently being used in production with the 1.0 micron 68030 mask sets. The F91C mask set is implemented using 0.8 micron technology at our Tohoku Facility (commonly referred to AS TSC) based in Sendai, Japan. TSC is one of the facilities that currently manufactures the existing 1.0 micron 68030 . Product manufactured from this new mask set will be functionally and electrically equivalent to the current production mask sets (C74N \& D66C). The end suffix of all part numbers will be changed from B to C for the F91C mask set.

A separate PCN, \#MPU-PCN-95-R00268, has already been issued for F91C in hermetic packages.
Contact customer service for samples.

## Reliability / Qualification Summary

TSC 0.8 Micron 68030 (F91C) Qualification Result
Life Test (6.0V, 125 Deg C)

| Wafer Lot\# | $\mathbf{1 6 8 ~ H r s}$ | $\mathbf{5 0 4}$ Hrs | $\mathbf{1 0 0 8} \mathbf{~ H r s}$ |
| :---: | :---: | :---: | :---: | :---: |
| A10001A | $0 / 77$ | $0 / 77$ | $0 / 77$ |
| A10002A | $0 / 77$ | $0 / 77$ | $0 / 77$ |
| A10003A | $0 / 77$ | $0 / 77$ | $0 / 77$ |

Bake (175 Deg C)

| Wafer Lot\# | $\mathbf{1 6 8}$ Hrs | $\mathbf{5 0 4} \mathbf{~ H r s}$ | $\mathbf{1 0 0 8} \mathbf{~ H r s}$ |
| :---: | :---: | :---: | :---: | :---: |
| A1000 |  |  |  |
| A1001A | $0 / 77$ | $0 / 77$ | $0 / 77$ |


| A10002A | $0 / 77$ | $0 / 77$ | $0 / 77$ |
| :---: | :---: | :---: | :---: |
| A10003A | $0 / 77$ | $0 / 77$ | $0 / 77$ |

## ESD (Human Body Model)

| Wafer Lot\# | $\mathbf{5 0 0} \mathbf{V}$ | $\mathbf{1 0 0 0} \mathbf{V}$ | $\mathbf{1 5 0 0} \mathbf{V}$ | $\mathbf{2 0 0 0} \mathbf{V}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10001A | $0 / 2$ | $0 / 2$ | $0 / 2$ | $\mid 3 / 3^{*}$ |
| A10001A | $0 / 2$ | $0 / 2$ | $0 / 2$ | $3 / 3^{*}$ |
| A 10003 | $0 / 2$ | $0 / 2$ | $0 / 2$ | $3 / 3^{*}$ |
| A10004B | - | - | - | $0 / 6$ |

*A layout error caused the first 3 lots to fail 3 input pins @ 2000 V ; correction was made and verified on Lot 4.

